

COMSOL Multiphysics[®] Based Simulation of Optical Response in Si Micro-line Array based Broadband Photodetector Fabricated on Silicon-on-insulator (SOI) Wafers V. K. Aggarwal¹, S. Sett¹, A. Singha² & A. K. Raychaudhuri¹

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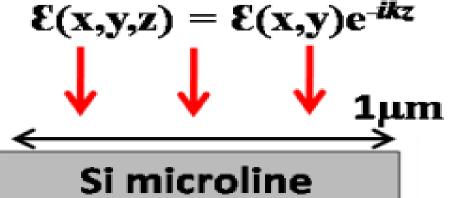
INTRODUCTION

•Commercially available photodetectors (Si/Ge) have Responsivity (R) < 0.6A/W

•In semiconductor nanowires like Si/ZnO/Ge it has been observed that R is in the range of 10^2 - 10^7 A/W

•Fabrication of photodetectors using nanowires is a complex process and in particular, it is not possible to have high production rate needed for commercial purpose in fabrication of devices made from single nanowires grown ex-situ bottom-up process.

$$J_{n}(r,t) = qn\mu_{n}\nabla\varphi_{c} + \mu_{n}k_{B}TK\left(\frac{n}{N_{c}}\right)\nabla n + (\frac{nq}{T})D_{n,th}\nabla T$$
$$J_{p}(r,t) = qp\mu_{p}\nabla\varphi_{v} + \mu_{p}k_{B}TK\left(\frac{p}{N_{v}}\right)\nabla p - (\frac{pq}{T})D_{p,th}\nabla T$$
$$\frac{\partial n}{\partial t} = \frac{1}{q}\left(\nabla J_{n}\right) - U_{n} \quad \text{and} \quad \frac{\partial p}{\partial t} = \frac{-1}{q}\left(\nabla J_{p}\right) - U_{p}$$
$$\varepsilon(\mathbf{x},\mathbf{y},\mathbf{z}) = \varepsilon(\mathbf{x},\mathbf{y})e^{-i\mathbf{k}\mathbf{z}}$$



HIGHLIGHTS

•We made an array of Si microlines (-1 μm wide) fabricated by a top-down process that is compatible with wafer level processing.

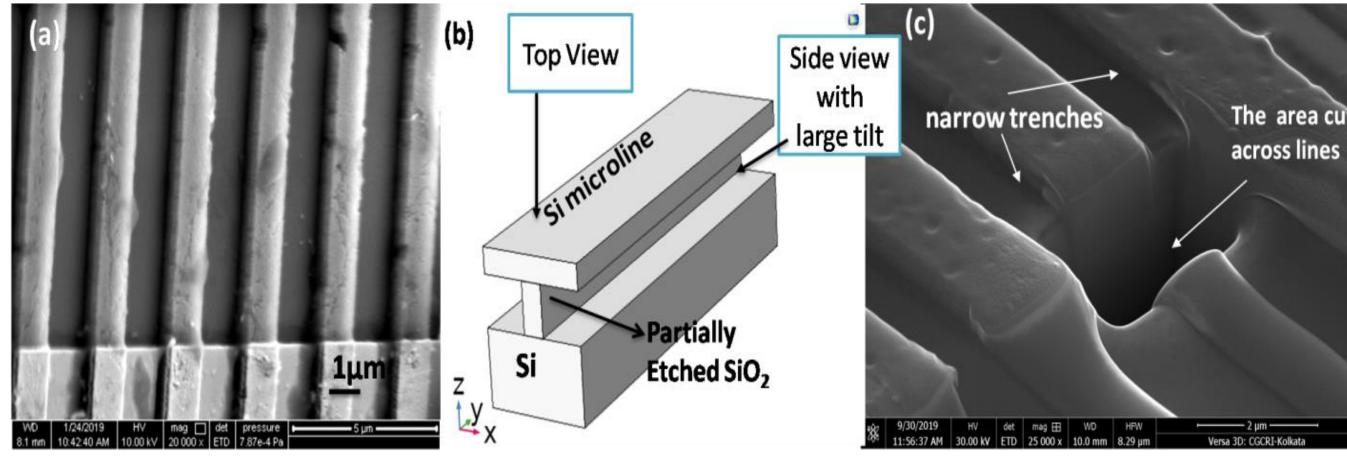


Figure 1. (a) FESEM image of array of Si microlines. (b) Schematic of the partially suspended device created by etching. (c) Magnified view showing partially suspended Si microlines.

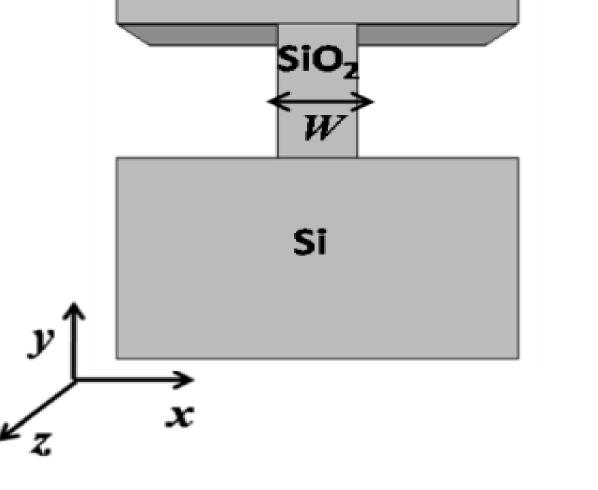
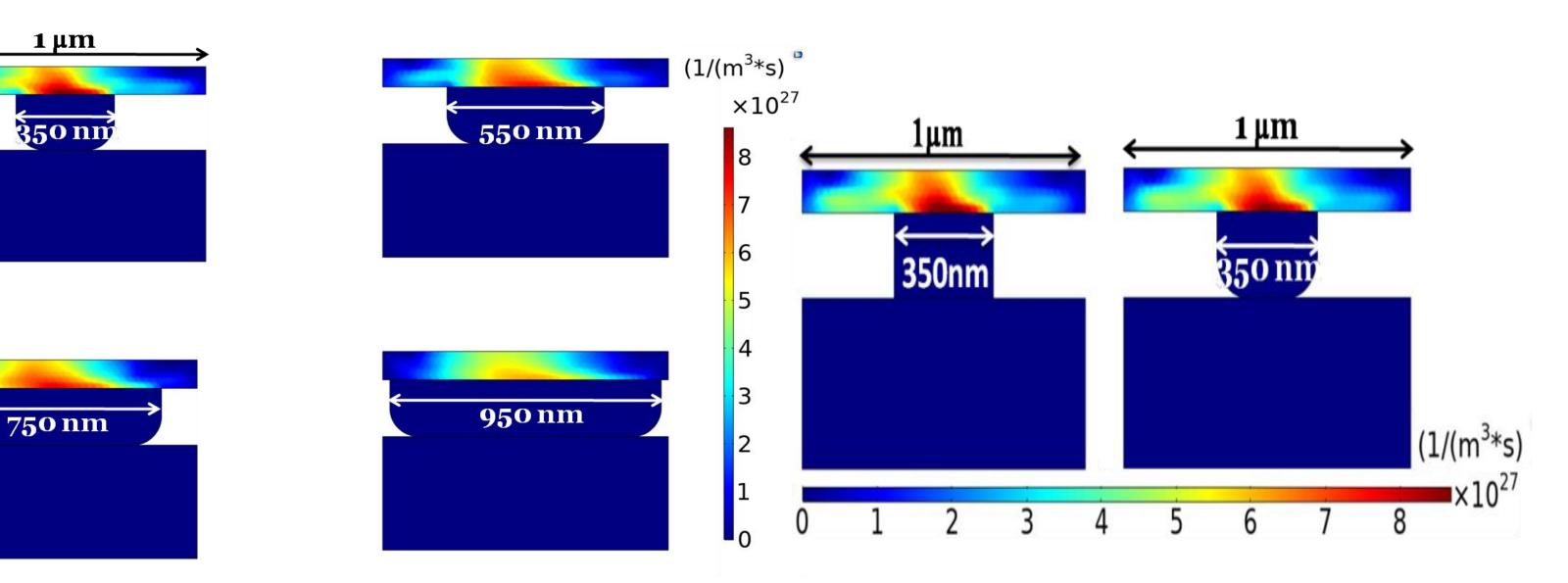


Figure 3. Cross-sectional image of device geometry of partially suspended Si microline used for simulation. The width W of the SiO₂ under-layer is a variable parameter in the simulation

CONFERENCE

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RESULTS



- Make Metal-semiconductor-metal (MSM) photodetectors from the Si microlines.
- Achieve a large responsivity (R) larger than that available in commercial p-n detectors (0.6 A/W) although less than that in a single Si NW device (10⁴ A/W).

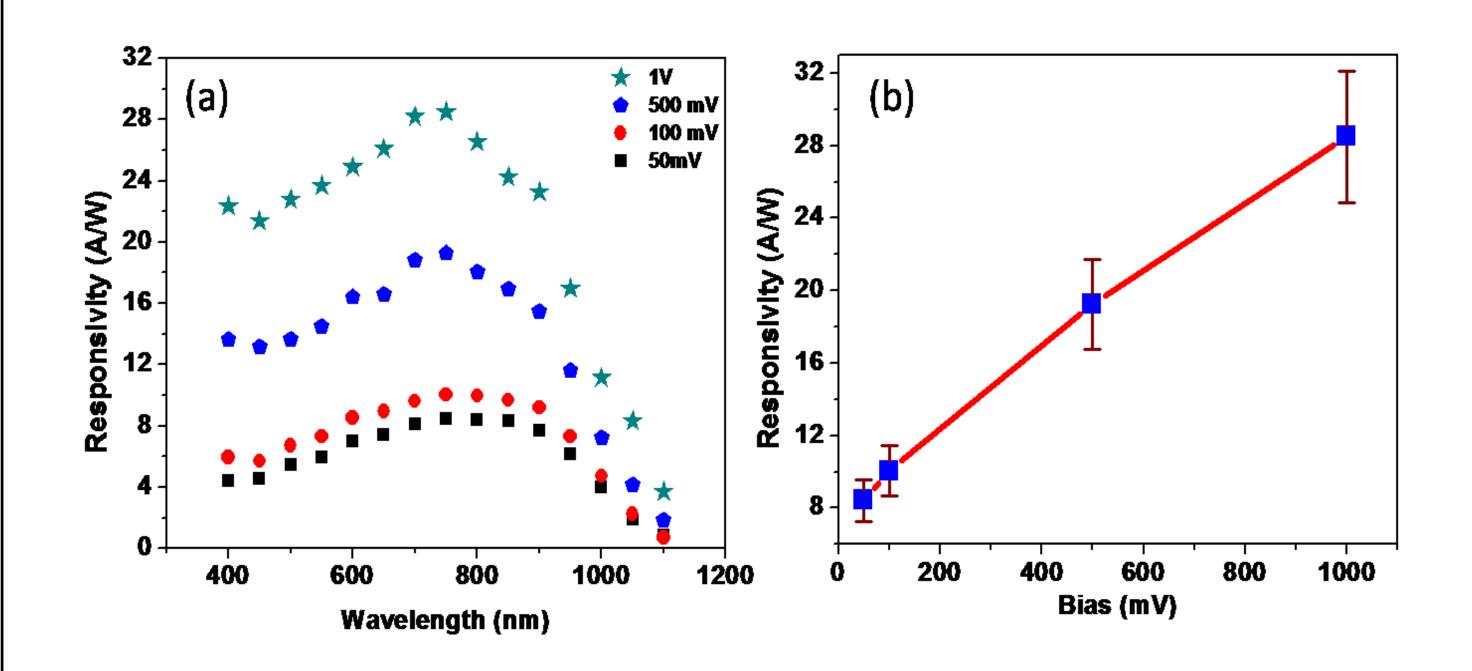


Figure 4. Surface plot of the carrier Recombination rate per unit volume in partially suspended Si microline with different W. Color legend is given alongside

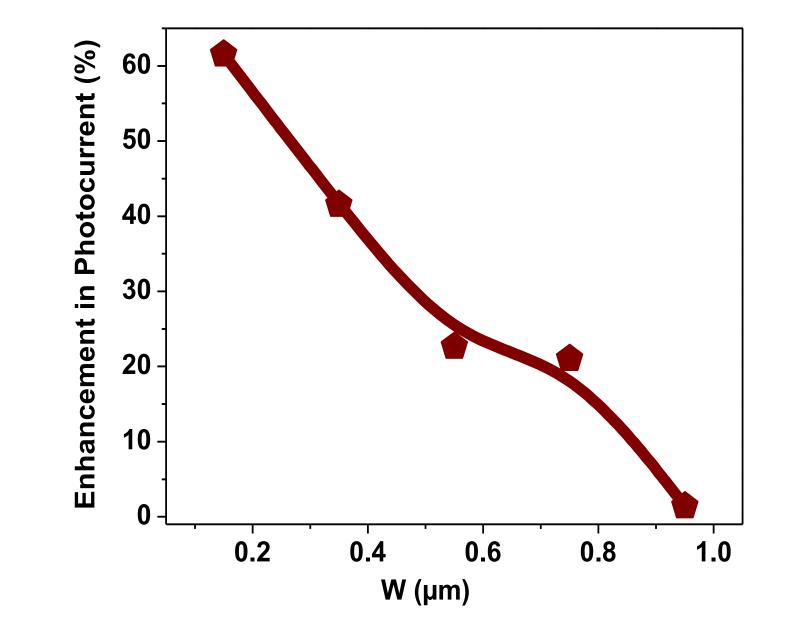


Figure 5. Surface plot of carrier Recombination rate per unit volume in partially suspended Si microline with *W*=350 nm (a) sharp anisotropic etch profile (b) lateral etch profile. Color legend is given below

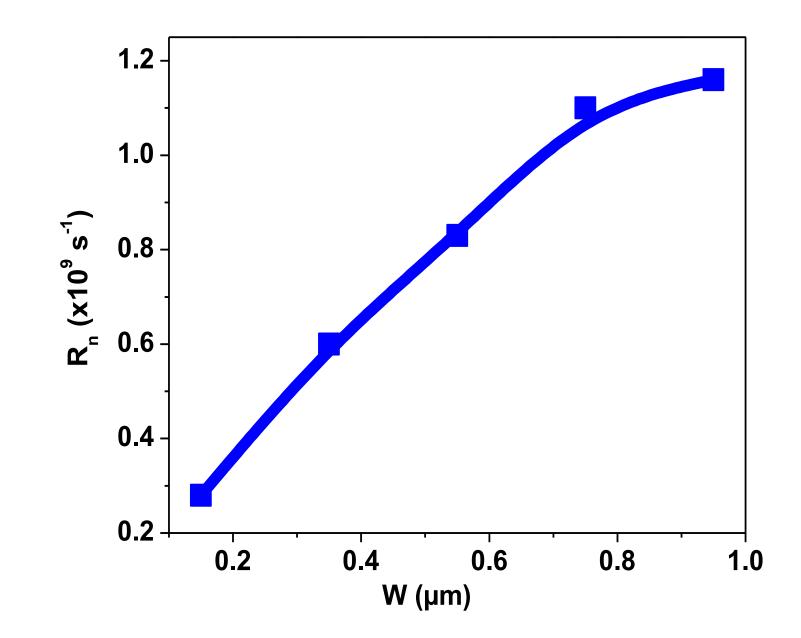


Figure 6. Dependence of the enhancement of photocurrent (over dark current) **Figure 7**. carrier recombination rate R_n on the width W of the underlying SiO₂ layer that supports the microline

CONCLUSIONS

Figure 2. (a) Responsivity as a function of wavelength at different values of applied bias. (b) Peak responsivity (λ=750 nm) as function of applied bias with intensity varying from 5 - 7 W/m².

COMPUTATIONAL METHOD

High responsivity from Si microline array as observed is due to its partial suspension which has been validated by the simulation using the semiconductor module coupled with AC/DC module using a frequency domain study. The Responsivity is at least an order higher than commercially available bulk Si detectors in the same spectral range.
Si microlines, which are partially suspended, prevent recombination of carriers during transit, thereby elongating its lifetime and has been validated by using COMSOL Multiphysics[®] simulation.

REFERENCES

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